

SUBJECT TEACHING GUIDE

327 - Systems, virtualization and safety

Master's Degree in computing engineering

Academic year 2024-2025

1. IDENTIFYING DATA					
Degree	Master's Degree in computing engineering			Type and Year	Compulsory. Year 1
Faculty	Faculty of Sciences				
Discipline	COMPUTER ENGINEERING				
Course unit title and code	327 - Systems, virtualization and safety				
Number of ECTS credits allocated	6	Term	Semester based (1)		
Web	https://github.com/valentinpuente/SVS				
Language of instruction	Spanish	English Friendly	No	Mode of delivery	Face-to-face

Department	DPTO. INGENIERÍA INFORMÁTICA Y ELECTRÓNICA				
Name of lecturer	VALENTIN PUENTE VARONA				
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Other lecturers					

3.1 LEARNING OUTCOMES
- Understanding the relevance of virtualization in modern infrastructures
- Understanding how virtualization influences the performance of computer systems.
- Explore the role of hardware components designed to enhance the security of computer systems.
- Understand the vulnerabilities associated with commonly used execution techniques in current computers and their level of criticality.

4. OBJECTIVES

The course is focused on providing students with the fundamental tools for the understanding and management of virtualization at the system level, as a key element for the deployment of cloud computing. The main approaches, from the hardware perspective to enhance the security of these environments will be introduced.

6. SUBJECT PROGRAM

CONTENTS

1	Introduction
2	Operating Systems. Direct limited execution model, CPU virtualization and memory virtualization. I/O and persistence.
3	Introduction to Virtualization. Virtualization without architectural support: Popek Goldberg's Theorem.
4	Hardware support for CPU and memory virtualization: x86 case
5	Input-output virtualization
6	Basic concepts of security, secure processors and root-of-trust.
7	Processor and Memory Protection. Side channel attacks and current hardware limitations.
8	Review of scientific papers
9	Final exam

7. ASSESSMENT METHODS AND CRITERIA

Description	Type	Final Eval.	Reassessn	%
Review of Scientific Papers	Others	No	Yes	40,00
Final Evaluation	Written exam	Yes	Yes	50,00
Students should participate in the discussion forums available on GitHub page. Participation may include resolving doubts about topics covered in class, asking questions related to upcoming scientific assignments, among other activities. The participation	Others	No	No	10,00
TOTAL				100,00
Observations				
If the quota of 'Matriculas de honor' of the course is completed in ordinary evaluation, students in the September call can't opt for one of them'				
Observations for part-time students				
Students enrolled part-time by the same method of assessment shall be governed students enrolled full-time.				

8. BIBLIOGRAPHY AND TEACHING MATERIALS

BASIC

E. Bugnion, J. Nieh, and D. Tsafirir, "Hardware and Software Support for Virtualization," Synth. Lect. Comput. Archit., vol. 12, no. 1, pp. 1–206, Feb. 2017.

J. Szefer, "Principles of secure processor architecture design," Synth. Lect. Comput. Archit., vol. 13, no. 3, pp. 1–173, 2018.

Operating Systems: Three Easy Pieces
Remzi H. Arpaci-Dusseau and Andrea C. Arpaci-Dusseau
Arpaci-Dusseau Books
March, 2018