

## SUBJECT TEACHING GUIDE

### G268 - Digital Systems

#### First Degree in Computer Systems Engineering

Academic year 2024-2025

1. IDENTIFYING DATA					
Degree	First Degree in Computer Systems Engineering			Type and Year	Core. Year 1
Faculty	Faculty of Sciences				
Discipline	Subject Area: Fundamentals of Computer Science Basic Training Module				
Course unit title and code	G268 - Digital Systems				
Number of ECTS credits allocated	6	Term	Semester based (1)		
Knowledge Field					
Web	<a href="https://aulavirtual.unican.es">https://aulavirtual.unican.es</a>				
Language of instruction	Spanish	English Friendly	Yes	Mode of delivery	Face-to-face

Department	DPTO. INGENIERÍA INFORMÁTICA Y ELECTRÓNICA				
Name of lecturer	JOSE ANGEL GREGORIO MONASTERIO				
E-mail	joseangel.gregorio@unican.es				
Office	Facultad de Ciencias. Planta: + 1. DESPACHO (1104)				
Other lecturers	PABLO ABAD FIDALGO PABLO PRIETO TORRALBO				

#### 4. OBJECTIVES

This subject introduces students to the basic blocks available for building digital systems, focusing on circuits that can be part of a computer. Its main objective is that students know and understand the functional blocks that they will find while studying each unit that makes Von Neumann model.

**6. SUBJECT PROGRAM**

## CONTENTS

1	Introduction <ul style="list-style-type: none"> <li>- Computer</li> <li>- Logic Circuits</li> <li>- Information</li> <li>- Coding</li> <li>- Analog and Digital Signals</li> <li>- Languages</li> <li>- Programs</li> </ul>
2	Natural Numbers <ul style="list-style-type: none"> <li>- Numbering system and basic operations</li> <li>- Integers</li> </ul>
3	Combinational logic circuits (CLC) <ul style="list-style-type: none"> <li>- Introduction, definition</li> <li>- Mathematical model</li> <li>- Logic Gates</li> <li>- Large CLCs connecting small ones</li> <li>- CLC Analysis</li> <li>- Boolean Algebra</li> <li>- Analysis and Synthesis using Boolean Algebra</li> <li>- Sum of minterms</li> <li>- Decoder</li> <li>- ROM</li> </ul>
4	Sequential logic <ul style="list-style-type: none"> <li>- Introduction, basic definitions</li> <li>- Memory needs, D flip-flop</li> <li>- Synchronization needs, clock</li> <li>- Mealy model, specification</li> <li>- More model, specification</li> <li>- Synthesis of sequential circuits</li> <li>- Analysis of sequential circuits</li> </ul>
5	Basics of a general-purpose processor <ul style="list-style-type: none"> <li>- General Processing Unit</li> <li>- Adding Data Memory</li> <li>- Explicit and Implicit Sequencing</li> <li>- Coding Control Signals</li> <li>- Instruction format</li> <li>- General Control Unit</li> </ul>

### 7. ASSESSMENT METHODS AND CRITERIA

Description	Type	Final Eval.	Reassessn	%
Midterm exam (teory&lab)	Laboratory evaluation	No	Yes	40,00
Final Exams (January and February)	Laboratory evaluation	Yes	Yes	60,00
TOTAL				100,00
Observations				
Observations for part-time students				
Students verifying these conditions and that do not take the midterm exams and/or do not pass the practices during the course, need to take a global test.				

### 8. BIBLIOGRAPHY AND TEACHING MATERIALS

#### BASIC

Digital Design and Computer Architecture (RISC-V ed.), S. Harris & D. Harris , ed. Morgan Kaufmann, 2022.